

PALM INTRANET

Day: Saturday Date: 10/30/2004 Time: 16:42:24

Inventor Name Search Result

Your Search was:

Last Name = YANG

First Name = JICHENG

Application# Patent#		Status	Date Filed	Title	Inventor Name 14		
<u>60468420</u>	Not Issued	159	05/07/2003	CERAMIC SUBSTRATE ASSEMBLY PROCESS USING A CARRIER	YANG, JICHENG		
10865096	Not Issued	018	06/09/2004	06/09/2004 SEMICONDUCTOR DEVICE AND ITS MANUFACTURING METHOD			
<u>10852316</u>	Not Issued	041	05/24/2004	METHOD TO PREVENT DIE ATTACH ADHESIVE CONTAMINATION IN STACKED CHIPS	YANG, JICHENG		
<u>10852315</u>	Not Issued	041	05/24/2004	METHOD TO PREVENT DIE ATTACH ADHESIVE CONTAMINATION IN STACKED CHIPS	YANG, JICHENG		
10845983	Not Issued	041	05/14/2004	COUPLING SPACED BOND PADS TO A CONTACT	YANG, JICHENG		
10666345	Not Issued	030	09/18/2003	MULTI-CHIP MODULE WITH EXTENSION	YANG, JICHENG		
10215571	6580160	150	08/08/2002	COUPLING SPACED BOND PADS TO A CONTACT	YANG, JICHENG		
10144905	6807218	150	05/13/2002	LASER MODULE AND OPTICAL SUBASSEMBLY	YANG, JICHENG		
09941315	6620648	150	08/29/2001	MULTI-CHIP MODULE WITH EXTENSION	YANG, JICHENG		
09703920	6759307	150	11/01/2000	METHOD TO PREVENT DIE ATTACH ADHESIVE CONTAMINATION IN STACKED CHIPS	YANG, JICHENG		
<u>09606480</u>	<u>6274930</u>	150	06/29/2000	MULTI-CHIP MODULE WITH STACKED DICE	YANG, ЛСНЕNG		
09437870	6453547	150	11/10/1999	COUPLING SPACED BOND PADS TO A CONTACT	YANG, ЛСНЕNG		
09437595	6207467	150	11/10/1999	MULTI-CHIP MODULE WITH	YANG,		

\$ \$ \$ \$ \$ \$ \$ \$ \$ \$			STACKED DICE	JICHENU
<u>09420817</u> No	ot Issued 163	10/19/1999	MULTI-CHIP MODULE WITH	YANG,
		A	EXTENSION	ЛСНЕNG

Inventor Search Completed: No Records to Display.

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Search Another:	YANG	JICHENG			
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Application No *	18666345	Patent No		ls	sue Date		Filing	Date 09/18/2003
Examiner No	78780 PEF	RKINS, PAMELA	E					
GAU	2822 Clas	s 438 St	ubclass 186	000	Status	40	Abandoned	
Inventor Name	YANG, JICHENG						FAOM Y	Disposal N
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Case History			_	Applicatio				
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